



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

II Year II Semester		L	T	P	C
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DIGITAL ELECTRONICS LAB					

Preamble:

The aim of this lab is to understand the Basics of digital electronics and able to design basic logic circuits, combinational and sequential circuits.

Course Objectives:

- To know the concept of Boolean laws for simplifying the digital circuits.
- To understand the concepts of flipflops.
- To understand the concepts of counters.
- To analyze and design various circuits.

List of Experiments:

Any TEN of the following Experiments are to be conducted

1. Verification of truth tables of Logic gates: Two input (i) OR (ii) AND (iii) NOR (iv) NAND (v) Exclusive OR (vi) Exclusive NOR
2. Design a simple combinational circuit and obtain minimal SOP expression and verify the truth table using Digital Trainer Kit
3. Verification of functional table of 3 to 8 line Decoder / De-multiplexer
4. 4 variable logic function verification using 8 to 1 multiplexer.
5. Design full adder circuit and verify its functional table.
6. Design full Subtractor circuit and verify its functional table.
7. Verification of functional tables of Flip-Flops
8. Design a four bit ring counter using D Flip – Flops / JK Flip Flop and verify output
9. Design a four bit Johnson's counter using D Flip-Flops / JK Flip Flops and verify output
10. Draw the circuit diagram of MOD-8 ripple counter and construct a circuit using T-Flip-Flops and Test it with a low frequency clock and Sketch the output waveforms.
11. Design MOD – 10 ripple counter using T- Flip-Flop and verify the result and Sketch the output waveforms
12. Design MOD – 8 synchronous counter using D Flip-Flop and verify the result and Sketch the output waveforms.

Course Outcomes: At the end of the course, student will be able to

- Learn the basics of gates, flip-flops and counters.
- Construct basic combinational circuits and verify their functionalities
- Apply the design procedures to design basic sequential circuits
- To understand the basic digital circuits and to verify their operation
- Apply Boolean laws to simplify the digital circuits.